Subject: Re: ASIO DRIVER current status?
Posted by mikeaudet on Sun, 23 Nov 2014 14:29:16 GMT

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Hi Will,

I actually don't want to criticize ID over this. There is a lot of good stuff in their work that I'm learning from. I don't think Ensoniq really had the money they needed to get this done right. We can certainly criticize ID over how they have handled the PACE thing, though.

I think just had a breakthrough - maybe there is a way to get these immediate buffers time aligned. I just loaded up a pro tools session at 128 samples, and it played back properly the first time, twice in a row. I'll have to do a bunch more tests to make sure all is well, but it's worked twice, and those are the only times it's ever worked.

I still have to fix the locking, and I have to make changes to the helper thread in the PSCL. It runs every 256 samples and does a bunch of housekeeping. But it touches resources my timer thread needs, and there is no time for contention at 64 sample buffer sizes. I'll need to make changes so that a special version runs when the ASIO thread attaches,

This	is	going	to	be	SO	coo	l!
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All the best!

Mike