
Subject: Update....

Posted by [mikeaudet](#) on Thu, 19 May 2022 15:00:55 GMT

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Hi Everyone,

It's been tough to make progress this year, but I'm finally getting traction. I've just started testing a version of the PSCL that is designed to work with jBridge. For those who haven't used jBridge, it allows a 32 bit host to connect with 64 bit VST files. I just started testing it with some 64 bit Waves plugins. So far, so good. The updated PSCL has a low priority thread that checks for jBridge processes and moves them off the CPU used by the PARIS application. This way, we can run 64 bit VSTs on all but one available CPU cores, while not running into the thread-safety issues in the PARIS application.

jBridge can be found here:

<https://jstuff.wordpress.com/jbridge/>

My next move is to work on the kernel driver (scherzo.sys). I bought a new ASUS motherboard with a PCI slot, and the driver does not work with this new board. The new board uses a I/O MMU, which is a new thing for PCs. I suspect that this is the problem, but I'll know more next week. An I/O MMU puts the PCI bus behind a memory controller that has to be programmed in order to allow PCI cards to access main memory. It's an extra level of indirection put in place for security reasons. The driver was not designed for this kind of thing.

I still haven't gotten the EV security certificate. The eventually rejected by documents that were signed by a justice of the peace. It's completely insane. An accountant or a lawyer will do just fine, but a judge can't be trusted. It's nuts. I'm going to go see a notary after I get the kernel driver updated. I only get a year on the certificate, so I think it makes sense to wait until I have this next phase done.

That's all I have to report for now.

All the best,
Mike

File Attachments

1) [PARIS_with_Waves.jpg](#), downloaded 8372 times

Edit Functions Settings EQ Aux Tracks Automation

The screenshot displays a mixer interface with 10 channels. Each channel has a 'BYPASS' button and a list of processing plugins: Abbey Road, CLA-2A Ster, and dbx-160 Ster. Below the bypass section, there are three EQ sections. Each EQ section includes a frequency knob (set to 1000 Hz), a gain knob (set to 0.0 dB), and a bandwidth knob (set to 1.5). There are also 'ALL EQ' and 'EQ ON' buttons. Below the EQ sections are stereo level meters for 'L' and 'R' channels, with a 'SOLO' and 'MUTE' button above them. At the bottom of each channel are 'REC' and 'AUTO' buttons. The time display at the bottom left shows 00:01:48.555.

The screenshot shows the IR-L convolution reverb plugin window. It includes a 'Full CPU' indicator and a list of parameters: Name (Hall - 1), Type (Concert Hall), Date (24 Mar 2004), SR (96000Hz -> 44100Hz), and Emitter (Genelec 530D). A table compares 'Original' and 'Current' values for Convolution (1.85s), RT60 (1.4s), Channels (4), and Size (11267). A frequency response graph is visible on the right. At the bottom, there are controls for 'Reverb Time' (0.000s), 'Conv. Length' (Full), 'Latency' (11ms), 'Dry/Wet' (100), 'Direct' (Off), and 'Output' levels (-2.8 and -2.6).

Options:

Post Notific

The screenshot shows a transport control window for 'Transport: [Untitled Project]'. It features standard playback buttons: Stop, Play/Pause, Record, and Next. Below these are buttons for 'P', 'M', 'S', 'L', and '0'. A 'PUNCH' button and a 'LOCK' button are also present. The time display shows 00:01:48:16.6 SMPTE and 00:00:00:00.0 SMPTE.

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