
Subject: Update....

Posted by [mikeaudet](#) on Thu, 19 May 2022 15:00:55 GMT

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Hi Everyone,

It's been tough to make progress this year, but I'm finally getting traction. I've just started testing a version of the PSCL that is designed to work with jBridge. For those who haven't used jBridge, it allows a 32 bit host to connect with 64 bit VST files. I just started testing it with some 64 bit Waves plugins. So far, so good. The updated PSCL has a low priority thread that checks for jBridge processes and moves them off the CPU used by the PARIS application. This way, we can run 64 bit VSTs on all but one available CPU cores, while not running into the thread-safety issues in the PARIS application.

jBridge can be found here:

<https://jstuff.wordpress.com/jbridge/>

My next move is to work on the kernel driver (scherzo.sys). I bought a new ASUS motherboard with a PCI slot, and the driver does not work with this new board. The new board uses a I/O MMU, which is a new thing for PCs. I suspect that this is the problem, but I'll know more next week. An I/O MMU puts the PCI bus behind a memory controller that has to be programmed in order to allow PCI cards to access main memory. It's an extra level of indirection put in place for security reasons. The driver was not designed for this kind of thing.

I still haven't gotten the EV security certificate. The eventually rejected by documents that were signed by a justice of the peace. It's completely insane. An accountant or a lawyer will do just fine, but a judge can't be trusted. It's nuts. I'm going to go see a notary after I get the kernel driver updated. I only get a year on the certificate, so I think it makes sense to wait until I have this next phase done.

That's all I have to report for now.

All the best,
Mike

File Attachments

1) [PARIS_with_Waves.jpg](#), downloaded 4608 times

Edit Functions Settings EQ Aux Tracks Automation

The screenshot displays a mixer interface for a project titled "Mixer/1-SubMix 1 (A) : [Untitled Project]". The interface is organized into several sections:

- Channel Headers:** Each of the ten channels has a "BYPASS" button and a list of EQ settings: "Abbey Road", "CLA-2A Ster", "dbx-160 Ster", and an empty slot.
- EQ Section:** Each channel has an "EQ 1 OFF" toggle, a frequency knob (set to 1000 Hz), a gain knob (set to 0.0 dB), and a bandwidth knob (set to 1.5).
- Global EQ:** Below the channel EQs, there are "ALL EQ" and "EQ ON" (with a blue indicator) toggles, and a "OPEN" button.
- Balance/Left-Right Controls:** Each channel has a balance knob and a "Solo Mute" section with two sliders and a "Solo" button. The balance knobs are labeled "L 100" or "R 100".
- Gain/Faders:** Each channel has a vertical fader with a green level indicator and a "Solo" button.
- Bottom Row:** Each channel has a "REC" button, an "AUTO" button, and a time display showing "00:01:48.555".

This is a screenshot of the "IR-L" convolution reverb plugin window. It displays the following information and controls:

- Name:** Hall - 1
- Type:** Concert Hall
- Date:** 24 Mar 2004
- SR:** 96000Hz -> 44100Hz
- Emitter:** Genelec S30D
- Convolutions Table:**

	Original	Current
Convolutions:	1.85s	1.85s
RT60:	1.4s	1.4s
Channels:	4	4
Size:	11267	11267
Distance:	13m	NA
- Graph:** A frequency response graph showing the reverb's characteristic decay over time, with a zoom range from 0.000Sec to 2.000Sec.
- Controls:** Includes a "Reverb Time" slider, "Conv. Start" (0.000s), "Conv. Length" (Full), "Latency" (11ms), "Dry/Wet" balance knob (set to 100), "Direct" (Off), "Output" level meters (set to 0.0), and "Pre-delay" controls.

Options:

Post Notifica

This is a screenshot of the transport control panel for a project titled "Transport: [Untitled Project]". It features standard playback controls and a time display:

- Buttons:** Transport controls including Stop, Previous, Play/Pause, Record, and Next.
- Time Display:** Shows a main time of "00:01:48:16.6" and a sub-time of "00:00:00:00.0".
- Labels:** Includes "P N S L 0" for track positions and "PUNCH" and "LOCK" buttons.
- Timecode:** Two SMPTE timecode fields are visible.

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