
Subject: PSCL update

Posted by [Mike Audet](#) on Sun, 14 Sep 2008 14:04:12 GMT

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Hi All,

Here's my latest build of the PSCL.

I thought I should write a bit about what the PSCL is and what it does.

There is a set chain of communication that goes on in PARIS while the app is running. It looks like this:

PARIS App <--> PSCL <--> scherzo driver <---> hardware.

Basically, the App calls functions in the PSCL in order to tell the hardware to stop playing, or start, or load the driver for the 8 out cards, or whatever.

The PSCL translates these requests to commands that the cards can understand and sends the commands to the scherzo driver to pass them down to the cards.

When the PSCL was first written - which was a long time ago now - there was no way to run PARIS on a multi-CPU machine. Not only was there no need to protect the code from the hazards of a multi-cpu machine, there was no way to test if what you had done worked even if you tried.

I've been trying to make the PSCL multi-cpu safe. This has been a huge challenge for me because the PSCL was written in a c-like style. It's all structures and functions. It's not object oriented at all, which is what is more common today and what I'm used to. I'm also still a new programmer. So, more than once I've thought something was broken or messed up when it probably wasn't. I just didn't understand it correctly.

Anyway, what I have done is put locks on all the resources I can find that could be affected by two CPUs trying to change them at the exact same time.

I've also discovered that there are certain card resources that the PSCL tries to change directly without going through the scherzo driver. These variables seem to need around 3 miliseconds to "take". I think that under windows 95, the PSCL was directly altering the memory on the cards, but Windows XP doesn't allow that. What I think is happening is that Windows is intercepting

the attempt to alter the variable and passing it down through the regular mechanisms, and that imposes a delay. If the app moves on and tries to do something that requires the value being set properly, things go wrong. I'm guess that on a single CPU system, Windows is regularly interrupting to manage memory, read files from the disk, update the clock, etc, etc., so these delays were "filled in" by windows. I'm just making them explicit.

Anyway, it seems to be working well for me. I also tightened up the start up hardware detection timings because some of them seem to work fine at one fifth what they were now that the direct writes are being managed.

There are other small changes, too. Let me know if you have a chance to try this and how it goes. There may be hardware configurations that don't like what I've done. But, my IF2 now work, and it wasn't working well as of the last build.

I hope this clears up any questions about what I'm doing. I'll try to package these changes into a proper installer once I'm done building, which will probably be soon. I think we're almost there.

All the best!

Mike

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```
---= linux48cd19dc--
```

This is a multi-part message in MIME format.

Looks like I trimmed the timings down too far in one place.

Here's a newer one.

"Mike Audet" <mike@...> wrote:

```
>  
>  
>  
>  
>Hi All,  
>  
>Here's my latest build of the PSCL.  
>  
>I thought I should write a bit about what the PSCL is and what it does.  
>  
>
```

>There is a set chain of communication that goes on in PARIS while the app
>is running. It looks like this:
>
>PARIS App <--> PSCL <--> scherzo driver <---> hardware.
>
>Basically, the App calls functions in the PSCL in order to tell the hardware
>to stop playing, or start, or load the driver for the 8 out cards, or whatever.
> The PSCL translates these requests to commands that the cards can understand
>and sends the commands to the scherzo driver to pass them down to the cards.
>
>
>When the PSCL was first written - which was a long time ago now - there
was
>no way to run PARIS on a multi-CPU machine. Not only was there no need
to
>protect the code from the hazards of a multi-cpu machine, there was no way
>to test if what you had done worked even if you tried.
>
>I've been trying to make the PSCL multi-cpu safe. This has been a huge
challenge
>for me because the PSCL was written in a c-like style. It's all structures
>and functions. It's not object oriented at all, which is what is more common
>today and what I'm used to. I'm also still a new programmer. So, more
than
>once I've thought something was broken or messed up when it probably wasn't.
> I just didn't understand it correctly.
>
>Anyway, what I have done is put locks on all the resources I can find that
>could be affected by two CPUs trying to change them at the exact same time.
> I've also discovered that there are certain card resources that the PSCL
>tries to change directly without going through the scherzo driver. These
>variables seem to need around 3 milliseconds to "take". I think that under
>windows 95, the PSCL was directly altering the memory on the cards, but
Windows
>XP doesn't allow that. What I think is happening is that Windows is intercepting
>the attempt to alter the variable and passing it down through the regular
>mechanisms, and that imposes a delay. If the app moves on and tries to
do
>something that requires the value being set properly, things go wrong.
I'm
>guess that on a single CPU system, Windows is regularly interrupting to
manage
>memory, read files from the disk, update the clock, etc., etc., so these
delays
>were "filled in" by windows. I'm just making them explicit.
>
>Anyway, it seems to be working well for me. I also tightened up the start
>up hardware detection timings because some of them seem to work fine at

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KjI2Mgg0EjQXNJl0tDTJNM401DTeNOU07zT3NCY1OzVPNWM1bDV5NXQ20DaF
N4o3kDeaN6E3qzcEOAk4DzgaOCE4Lzg2OEA4hTiJOdc5qDu1O3Y+kT7TPgl/
aj+tP7Q/xz/xPwBgAQAUQAATzBfMHkyGDMfM6Q0qDSsNLA0tDS4NLw0wDTU
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nTu3O8Y70jvnO/s7BDwbPDI8VzxcPHM8hJyLPKc8tDzEPN487zz0PAE9Ej0i

Page 161 of 182 ---- Generated from The PARIS Forums

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---= linux48cd46ec--
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Subject: Re: PSCL update
Posted by [mike audet\[1\]](#) on Mon, 15 Sep 2008 14:11:46 GMT
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Thanks, Chuck! I've been reading up on volatile variables. I'm thinking that some of the delays I'm having to put in might be better handled by declaring the user mode pointers as volatile given that we're really multitasking now.

This is a great learning experience for me. :)

Thanks again!

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Subject: Re: PSCL update

Posted by [kerryg](#) on Mon, 15 Sep 2008 16:44:36 GMT

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Hey Chuck - that might come in awfully handy combined with this:

<http://www.reaper.fm/sdk/plugin/>

- Kerry

ps, ever get tempted to throw a PARIS rig back together? 2008 might be a fun time to do it.

On 2008-09-15 03:18:29 -0700, "chuck duffy" <c@c.com> said:

> Lookin good dude. I'm trying to dig up the C16 code for you, I'll bet you
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Subject: Re: PSCL update

Posted by [Aaron Allen](#) on Mon, 15 Sep 2008 17:59:31 GMT

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Subject: Re: PSCL update

Posted by [Rod Lincoln](#) on Mon, 15 Sep 2008 18:21:46 GMT

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Hey Aaron, or anybody for that matter...any thoughts on what a good multicore mobo to get would be? I just might have to do this.

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Subject: Re: PSCL update
Posted by [kerryg](#) on Mon, 15 Sep 2008 18:23:30 GMT
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You have laaaarge brain. My brain smallllll and scrawwwwwwny.

- Kerry

On 2008-09-15 07:11:46 -0700, "Mike Audet" <mike@....> said:

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Subject: Re: PSCL update
Posted by [kerryg](#) on Mon, 15 Sep 2008 19:14:14 GMT
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We can probably begin getting answers to that question now; now that it's stable on Mike's own system, it looks like testing with a range of different systems can begin in earnest.

I think if we're going to do this, it would be good to have a systematic way of documenting it this time. The time-honored system of posting them here in the NG is great and folks will undoubtedly be doing that, but a narrative-based system is painful to sift for concrete info (I know, having recently done it to find a stable configuration for myself).

So to that end, I'm working hard to figure out enough php, MySQL, and other frontend/backend stuff (that hurts my brain to think about) to create a database of PARIS system configurations where we can document stable systems - both past systems and the many potential new systems that this has suddenly enabled. The idea would be that you could go to

the Wiki and write your system info in and it would turn up in a database for everyone else to search.

We'll see how far I get. Unfortunately my three qualifications for the stuff I've taken on are 1) a lifelong inability to accept the statement "it's just that way" at face value, 2) a willingness to learn new things and 3) a bit of free time; any technical qualifications I'm having to pick up as we go.

We're off into the future here, folks - and it feels pretty good!

- Kerry

On 2008-09-15 11:21:46 -0700, "Rod Lincoln" <rlincoln@nospam.kc.rr.com> said:

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Subject: Re: PSCL update

Posted by [Aaron Allen](#) on Tue, 16 Sep 2008 02:27:57 GMT

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At this point I'd play it safe and stick to all Intel hardware with WinXP. Of course, fringe tweakheads like me are going to have to try the AMD thing most likely at some point. Most of my stuff here is really kind of old to be honest..... since I got out of the studio for a living deal I quit needing much horsepower. And to be honest, the multicore thing really is much more about native plugs than anything else, probably followed by improvements in hard drive speeds/throughput. Having a DSP system still rocks in this day and age, though native's become usable. I have a quad core/intel setup, but the one PCI it has is already populated so the next best thing is probably going to be that system I bought off of

Deej that's nVidia/AMD. I gotta get through 2 major projects though before I go there.

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> Rod

> "Aaron Allen" <know-spam@not_here.dude> wrote:

>>I've said it before, but it's worth another round. You absolutely fail to

>

>>suck bro. Awesome work, I can't wait to get a multi core rig going with

> some

>>EDS's in it to bust a trial off.

>>AA

>>

>>

>>"Mike Audet" <mike@....> wrote in message news:48ce6d22\$1@linux...

>>>

>>> Thanks, Chuck! I've been reading up on volatile variables. I'm

>>> thinking

>>> that some of the delays I'm having to put in might be better handled by

>

>>> declaring

>>> the user mode pointers as volatile given that we're really multitasking

>

>>> now.

>>>

>>> This is a great learning experience for me. :)

>>>

>>> Thanks again!

>>>

>>> Mike

>>>

>>>

>>>

>>> "chuck duffy" <c@c.com> wrote:

>>>>

>>>>Lookin good dude. I'm trying to dig up the C16 code for you, I'll bet

> you

>>>>could have fun with that :-)

>>>>

>>>>Chuck

```

>>>>
>>>>"Mike Audet" <mike@...> wrote:
>>>>
>>>>
>>>>
>>>>Looks like I trimmed the timings down too far in one place.
>>>>
>>>>Here's a newer one.
>>>>
>>>>
>>>>
>>>>
>>>>"Mike Audet" <mike@...> wrote:
>>>>>
>>>>>
>>>>>
>>>>>Hi All,
>>>>>
>>>>>Here's my latest build of the PSCL.
>>>>>
>>>>>I thought I should write a bit about what the PSCL is and what it
>>>>>does.
>>>>>
>>>>>
>>>>>
>>>>>There is a set chain of communication that goes on in PARIS while the
>>> app
>>>>>is running. It looks like this:
>>>>>
>>>>>PARIS App <--> PSCL <--> scherzo driver <---> hardware.
>>>>>
>>>>>Basically, the App calls functions in the PSCL in order to tell the
>
>>>>>hardware
>>>>>to stop playing, or start, or load the driver for the 8 out cards, or
>>> whatever.
>>>>> The PSCL translates these requests to commands that the cards can
>>>>> understand
>>>>>and sends the commands to the scherzo driver to pass them down to the
>>> cards.
>>>>>
>>>>>
>>>>>When the PSCL was first written - which was a long time ago now -
>>>>>there
>>>>>was
>>>>>no way to run PARIS on a multi-CPU machine. Not only was there no
>>>>>need
>>>>>to

```

>>>>>protect the code from the hazards of a multi-cpu machine, there was
> no
>>>>way
>>>>>to test if what you had done worked even if you tried.
>>>>>
>>>>>I've been trying to make the PSCL multi-cpu safe. This has been a
>>>>>huge
>>>>>challenge
>>>>>for me because the PSCL was written in a c-like style. It's all
>>>>>structures
>>>>>and functions. It's not object oriented at all, which is what is more
>>>>common
>>>>>today and what I'm used to. I'm also still a new programmer. So,
>>>>>more
>>>>>than
>>>>>once I've thought something was broken or messed up when it probably
>
>>>>>wasn't.
>>>>> I just didn't understand it correctly.
>>>>>
>>>>>Anyway, what I have done is put locks on all the resources I can find
>>> that
>>>>>could be affected by two CPUs trying to change them at the exact same
>>> time.
>>>>> I've also discovered that there are certain card resources that the
>
>>>>> PSCL
>>>>>tries to change directly without going through the scherzo driver.
>>>>>These
>>>>>variables seem to need around 3 miliseconds to "take". I think that
>
>>>>>under
>>>>>>windows 95, the PSCL was directly altering the memory on the cards,
> but
>>>>>Windows
>>>>>>XP doesn't allow that. What I think is happening is that Windows is
>
>>>>>>intercepting
>>>>>>the attempt to alter the variable and passing it down through the
>>>>>>regular
>>>>>>mechanisms, and that imposes a delay. If the app moves on and tries
> to
>>>>>>do
>>>>>>something that requires the value being set properly, things go wrong.
>>>>
>>>>>I'm
>>>>>>guess that on a single CPU system, Windows is regularly interrupting
> to

>>>>>manage
>>>>>memory, read files from the disk, update the clock, etc, etc., so
>>>>>these
>>>>>delays
>>>>>were "filled in" by windows. I'm just making them explicit.
>>>>>
>>>>>Anyway, it seems to be working well for me. I also tightened up the
>
>>>>>start
>>>>>up hardware detection timings because some of them seem to work fine
> at
>>>>>one
>>>>>fifth what they were now that the direct writes are being managed.
>>>>>
>>>>>There are other small changes, too. Let me know if you have a chance
>>> to
>>>>>try this and how it goes. There may be hardware configurations that
>
>>>>>don't
>>>>>like what I've done. But, my IF2 now work, and it wasn't working well
>>>>as
>>>>>of the last build.
>>>>>
>>>>>I hope this clears up any questions about what I'm doing. I'll try
> to
>>>>package
>>>>>these changes into a proper installer once I'm done building, which
> will
>>>>>probably be soon. I think we're almost there.
>>>>>
>>>>>All the best!
>>>>>
>>>>>Mike
>>>>>
>>>>
>>>
>>>
>>
>>
>>
>
